



For GPIO layout, see page 17 of the DE0_nano manual.

H-Bridge board:

VFPGA := FPGA voltage (pin 11 of GPIO_0 head- VCC_SYS)

GND & GND/sensor := should be grounded (connected to pin 12 of GPIO_0 head)

Since, we use only 1 motor right now, use channel 1 & 2 only.

ENA := VFPGA

ENB := GND

PWM_1: GPIO_0[1]

PWM_2: GPIO_0[3]

Motor:

Polulu: 1445

EncoderA: GPIO_0[5]

EncoderB: GPIO_0[7]