



#### **Interrupts vs. Polling**



# Polling

 Constantly reading a memory location, in order receive updates of an input value



#### Interrupts

- "Normal" microcontroller flow of control
  - $\circ$  Single process

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- $\circ$  Main routine / main loop
- Normal control flow statements (if, while, for, etc.)

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- Asynchronous, important events
  - May need to be handled immediately
  - Realtime requirements
  - Interrupt normal flow of control

## **Interrupt Handling**

- Processor interrupts (preempts) the current flow of control
- Time spent in interrupt handlers should be kept as short as possible
- Microcontroller offers interrupts for various conditions
  - Not all are useful all the time: enable required interrupts
  - Some critical may require atomic execution (no interruptions guaranteed)
  - $\,\circ\,$  Disable / re-enable interrupts around critical section

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#### In details





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#### In details

 Saving context: Push all temporary variables (like program counter) into stack



#### In details

 Construct context: Pull all temporary variables (like program counter) out of stack



## **Sources of Interrupts**

- Timers: System "ticks", periodic tasks
- Communications
  - $\circ$  Ethernet
  - o USB
  - $\circ$  Serial
- Periphery
  - E.g. ADC (Conversion complete)
  - Memory management
- Software

Software interrupts (trap instructions) / illegal instructions

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8

Reset / Power-On

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#### **Interrupt Priorities**

- What happens if a interrupt occurs during another interrupt is handled?
  - E.g. Timer ticks while Ethernet packet is processed
  - Priorities: Some interrupts are more important than other
  - Hierarchy of interrupts

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- Only more important interrupts may preempt lesser interrupts
- Usually: lower priority is more important
- Interrupts on same priority level do not preempt

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- Not immediately handled interrupts may be stored or may be lost
  - Depends if interrupt condition persists

#### **Interrupt Latency**

- Latency
  - $\,\circ\,$  Time passed between occurrence of event and handling
  - $\,\circ\,$  Interrupt is generated and source is serviced
- Interrupt latency
  - $\,\circ\,$  Interrupts tightly integrated into processor
  - Low latency

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 Complete current instruction, save registers and jump to handler

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10

Measured in instruction cycles

#### **Interrupts vs. Polling**

- Polling:
  - Continuously poll IOs for change of value
  - o Pro:
    - Short latencies (if low #IOs)
    - Events do not block the normal program exec.
  - Cons:
    - Most polls are unneeded value did not change
    - High CPU usage
    - Reaction time depends on #IOs
- Interrupt
  - Normal execution is interrupted when event occurs
  - o Pro:
    - Processor resources are only used when necessary
  - o Cons:
    - Program execution is interrupted in a non-deterministic manner

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#### **Interrupt Service Routine (ISR)**

Event handler for interrupt

Special, user-defined function for handling the interrupt





#### **Trigger for external interrupts**

#### Edge-triggered

- Falling Edge when signal value decreases
  Rising Edge when signal value increases
- Either Edge
- Level-triggered

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- each instance, when signal is above or below a certain threshold
- IRQs can be configured from within SOPC Builder

#### **Altera Interrupts**

- Initialization
- Enable the interrupt for the specific input IOWR\_ALTERA\_AVALON\_PIO\_IRQ\_MASK(<BASE>, <MASK>);
- Set the edge capability IOWR\_ALTERA\_AVALON\_PIO\_EDGE\_CAP(<BASE>, <VAL>)

#### Register handler

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#### Tasks

- Try out and understand the Interrupt based KEY-LED package
- Your code need to print "No Key is pressed!" when no key was pressed. At the same time, you code need to reponse the key press event and turn on and off the led alternately.

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15

Tell what you see and explain it.

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